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PATENT APPLICATION  
Docket No. 4591-170  
Client No. IE10189-US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Jong-Ho Lee and Nae-In Lee

Serial No. 09/776,059 Examiner: Douglas Wille

Filed: February 2, 2001 Art Unit: 2814

For: DIELECTRIC LAYER FOR SEMICONDUCTOR DEVICE  
AND METHOD OF MANUFACTURING THE SAME

Confirmation No. 9369

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Prior to Examination, please amend the application as follows.

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I hereby certify that this correspondence  
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Signature

Adrianne Chocholak

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